

ABSTRACT OF THE DISCLOSURE

The divided one-dimensional solid-state imaging device includes photodiode arrays having photodiodes for individual pixels arrayed in a line, output transistors for outputting electric charges from the respective photodiodes and transfer paths for transferring the outputted electric charges to output terminals. In the imaging device, the photodiode arrays is divided into a plurality of sections differing in the number of pixels in a photodiode arraying direction, the respective divided sections connecting to the corresponding ones of the transfer paths and the output terminals, and pixel rows in the respective divided sections are read out in parallel at the same time from the output terminals corresponding to the respective divided sections.

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